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PRESIDENCY UNIVERSITY

BENGALURU

Mid - Term Examinations – October 2025

Date: 08-10-2025

Time: 09.30am to 11.00am

School: SOCSE	Program: Common to all branches	
Course Code : CSE2257	Course Name: Computer Organization and Architecture	
Semester: III	Max Marks: 50	Weightage: 25%

CO - Levels	CO1	CO2	CO3	CO4	CO5
Marks	44	46	-	-	-

Instructions:

- (i) *Read all questions carefully and answer accordingly.*
- (ii) *Do not write anything on the question paper other than roll number.*

Part A

Answer ALL the Questions. Each question carries 2marks.

5Q x 2M=10M

1	What is the role of IR and PC?	2 Marks	L1	CO1
2	State different methods of representing a number.	2 Marks	L1	CO1
3	Describe the operations used in a stack	2 Marks	L2	CO2
4	What is Subroutine Nesting?	2 Marks	L2	CO2
5	List any five types of addressing modes.	2 Marks	L2	CO2

Part B

Answer the Questions.

Total Marks 40M

6.	a.	Illustrate the types of byte addressing with example.	10 Marks	L1	CO1
	b.	Explain in detail about basic instruction types and differentiate between RISC and CISC instruction sets.	10 Marks	L1	CO1

Or

7.	a.	With neat sketch, explain how the memory and processor are connected.	10 Marks	L1	CO1
	b.	Explain bus structure and the functional blocks of computer with neat diagram.	10 Marks	L1	CO1

8.	a.	Explain safe Push and safe Pop operation. Compare the Speed, size and cost of memory devices.	10 Marks	L2	CO2
	b.	Discuss about subroutine instructions and nesting.	10 Marks	L2	CO2

Or

9.	a.	Differentiate Static memory and Dynamic memory with its diagrams.	10 Marks	L2	CO2
	b.	Explain the organization of a 1K*1 memory chip with neat diagram.	10 Marks	L2	CO2