



Roll No.																			
----------	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

PRESIDENCY UNIVERSITY

BENGALURU

Mid - Term Examinations - March 2026

Date: 13-03-2026

Time: 11.45am to 01.15pm

School: SOCSE	Program: B.Tech-CSE,IST,CSI,COM,CSN,CAI,CCS,CIT,&CSG		
Course Code :CSE2269	Course Name: Operating Systems		
Semester: IV	Max Marks: 50	Weightage: 25%	

CO - Levels	C01	C02	C03	C04	C05
Marks	24	26			

Instructions:

- (i) Read all questions carefully and answer accordingly.
- (ii) Do not write anything on the question paper other than roll number.

Part A

Answer ALL the Questions. Each question carries 2marks.

5Q x2M=10M

1	Name the four main components of a computer system.	2 Marks	L1	C01
2	Differentiate between system software and application software, providing one example for each.	2 Marks	L2	C01
3	What is a thread?	2 Marks	L1	C02
4	List the key difference between a program and a process.	2 Marks	L1	C02
5	Describe the concept of a context switch.	2 Marks	L2	C02

Part B

Answer the Questions.

Total Marks 40M

6.	a.	Describe the dual role of an operating system from both the user view and the system view. Describe how the OS acts as a resource allocator and a control program from the system's perspective.	10 Marks	L2	C01
Or					
7.	a.	Explain the complete process of how a system call is handled by the operating system, from the moment a user application invokes it to when control is returned and use a diagram to illustrate the process.	10 Marks	L2	C01

8.	a.	Compare the monolithic, layered, and microkernel operating system structures. Discuss the primary advantages and disadvantages of each structure in terms of performance, reliability, and kernel size.	10 Marks	L2	C01
----	----	---	-------------	----	-----

Or

9.	a.	Clarify the key process management system calls in UNIX: fork(), exec(), wait(), and exit(). Describe the function of each call and how they work together to create and manage a new process.	10 Marks	L2	C01
----	----	--	-------------	----	-----

10.	a.	Describe the role of the Process Control Block (PCB) in an operating system. Explain in detail the various types of information stored in the PCB and how the operating system uses this information to manage processes.	10 Marks	L2	C02
-----	----	---	-------------	----	-----

Or

11.	a.	Differentiate between the shared memory and message passing models of Inter-Process Communication (IPC). Explain the advantages and disadvantages of each model and provide a scenario where each would be the preferred method of communication.	10 Marks	L2	C02
-----	----	---	-------------	----	-----

12.	a.	<p>Consider the following four processes with their respective arrival times, CPU burst times, and priorities:</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Process</th> <th>Arrival Time (ms)</th> <th>Burst Time (ms)</th> <th>Priority</th> </tr> </thead> <tbody> <tr> <td>P1</td> <td>0</td> <td>10</td> <td>2</td> </tr> <tr> <td>P2</td> <td>1</td> <td>6</td> <td>1</td> </tr> <tr> <td>P3</td> <td>3</td> <td>14</td> <td>4</td> </tr> <tr> <td>P4</td> <td>5</td> <td>8</td> <td>3</td> </tr> </tbody> </table> <p>Draw the Gantt charts for the execution of these processes using both the preemptive Priority Scheduling and Round Robin (RR) algorithms. Assume a time quantum of 4 ms for the Round Robin algorithm. For each algorithm calculate the Waiting-time, Finish-time and Turnaround time, Calculate the average waiting time and average turnaround time for both preemptive Priority Scheduling and Round Robin. Based on your results, compare the performance of the two algorithms in this specific scenario.</p>	Process	Arrival Time (ms)	Burst Time (ms)	Priority	P1	0	10	2	P2	1	6	1	P3	3	14	4	P4	5	8	3	10 Marks	L2	C02
Process	Arrival Time (ms)	Burst Time (ms)	Priority																						
P1	0	10	2																						
P2	1	6	1																						
P3	3	14	4																						
P4	5	8	3																						

Or

13.	a.	Describe the concept of threads and multithreading models. Explain the key difference between user-level threads and kernel-level threads, and discuss one-to-one and many-to-one models.	10 Marks	L2	C02
-----	----	---	-------------	----	-----