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PRESIDENCY UNIVERSITY BENGALURU

SCHOOL OF ENGINEERING END TERM EXAMINATION – AUGUST 2024

Semester : IV	Date: 6th August 2024
Course Code : ECE3001	Time: 09:30 A.M. – 12:30 P.M.
Course Name :Linear Integrated Circuits	Max Marks :100
Program :B. Tech. (ECE)	Weightage :50%

Instructions:

- (i) Read all questions carefully and answer accordingly.
- (ii) Question paper consists of 3 parts.
- (iii) Scientific and non-programmable calculator are permitted.
- (iv) Do not write any information on the question paper other than Roll Number.
- (v) Use semilog sheet to plot the magnitude response

PART A				
ANSWER ANY 3 QUESTIONS		$3Q \times 5M = 15 M$		
1	List 5 properties of an ideal op-amp	(CO 1)	[Knowledge]	
2	Define the following parameters a. Slew rate b. CMRR List the value of the above parameters for an ideal op-amp and 741 IC	(CO 1)	[Knowledge]	
3	Draw the circuit of a non-inverting comparator which will compare $v_{in}(t) = 10 \sin(200\pi t)$ with a reference voltage of 3 V. Draw the input waveform and the corresponding output waveform and the transfer characteristics.	(CO 3)	[Knowledge]	
4	Draw the circuit of a voltage to current converter with grounded load and derive the expression for the load current.	(CO 2)	[Knowledge]	
5	Draw the circuit diagram of a current to voltage converter and explain.	(CO 1)	[Knowledge]	

	PART B			
	ANSWER ANY 2 QUESTIONS	$2Q \times 20M = 40M$		
6	Draw the circuit diagram of an inverting Schmitt trigger for a case in which the absolute value of UTP and LTP will not be the same. Derive an expression for the triggering voltages. Estimate the value of the components to be used if $V_{LTP} = -1 V$ and $V_{UTP} = 8 V$. Draw the input waveform, the corresponding output waveform and the transfer characteristics if $v_{in}(t) = 10 \sin(400\pi t)$. Let $V_{sat} = 12 V$		[Comprehension]	
7	Draw the circuit diagram of a first order low pass filter. Estimate the component values to be used if the cut off frequency required is 1200 Hz and a pass band gain of 2.	(CO 3)	[Comprehension]	

	Draw the magnitude response of the filter in a semilog sheet considering the following frequencies.		
	Find the steady state output when the following input is applied to the filter		
	$v_{in}(t) = 4 + 2\sin(20\pi t) + 2\sin(2 \times 10^6 \pi t)$		
8	Draw the circuit diagram of a 555 timer which can be used to generate a 500 Hz square wave with a duty cycle of 80%. Explain the working of the circuit with the help of the block diagram. Estimate the components values to be used. Draw the output voltage and the voltage across the capacitor. Use a capacitor of $10\mu F$ and $V_{cc} = 12 V$	(CO 3)	[Comprehension]

	PART C		
	ANSWER ANY 3 QUESTIONS		5M=45M
9	Draw the circuit diagram of a 2 bit flash ADC and explain. Design the encoder also and show the final circuit.	(CO 3)	[Application]
10	Draw the circuit diagram of an instrumentation amplifier. Derive an expression for the output voltage. Estimate the component values to be used if the gain is to be between 200 and 800.	(CO 2)	[Application]
11	Draw the circuit diagram of a 3 bit R-2R DAC. Explain the working. Tabulate the output voltage if the feedback resistor used is 2R and the reference voltage is 2 V.	(CO 2)	[Application]
12	Draw the circuit diagram of a square wave generator using op-amp for a frequency of 2000 Hz. Explain the working of the circuit. Draw the output voltage and the voltage across the capacitor. Use $C = 0.01 \mu F$ and $V_{sat} = 15 V$	(CO 2)	[Application]